

Supplement to

Logic and Computer
Design Fundamentals
4rd Edition¹

INSTRUCTOR'S MANUAL – ERRATA

Version 4-1.0(11/28/2007)

- p. 10, Problem 1-17, line 1: Add (Errata: Replace “1480” with “1460”)
line 6: Replace “23” with “21”
line 7: Replace “1480” with “1460”
line 8: Replace “23” with “21”
line 9: Replace “15” with “13”
- p. 81, Problem 5-46, line 1: Change “(Errata: Replace “5-10” with “5-11”)” to
“(Errata: Replace “5-11” with “5-10”)”
- p. 90, Problem 6-10*, part (e): Express final answer as “1,333 MHz” as specified in
the problem.
- p. 129, Problem 11-9: Remove “(12-9C)”
- p. 142, Problem 12-19: Change “ $A\bar{1},1$ ” to “ A_1 ,”

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