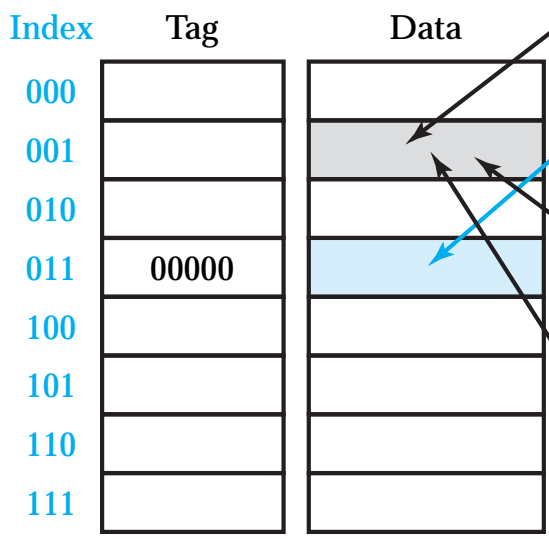
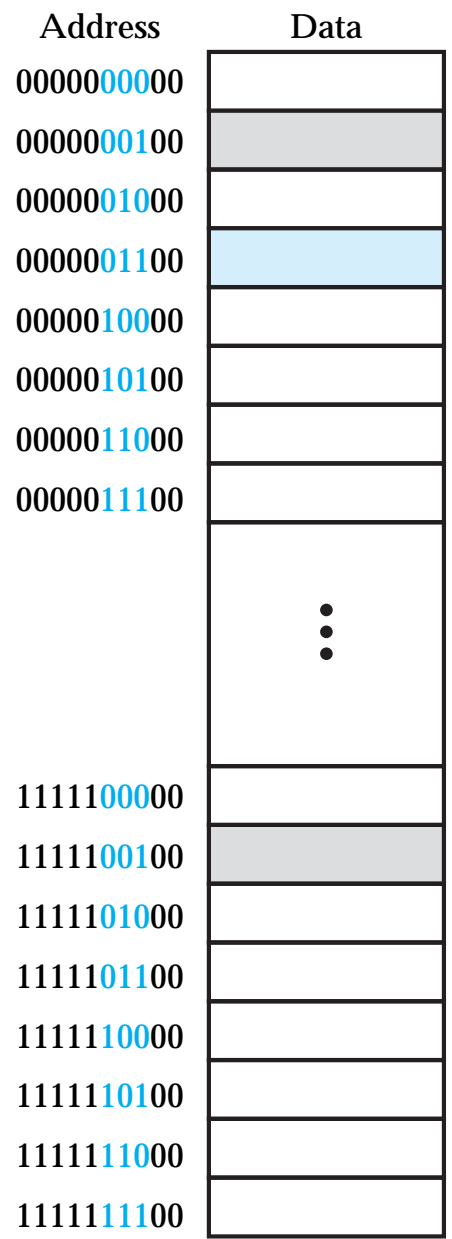


(a) Memory address

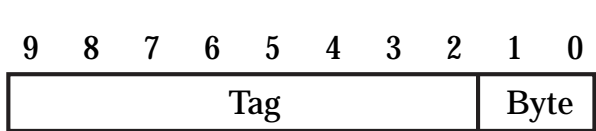


Cache



Main memory

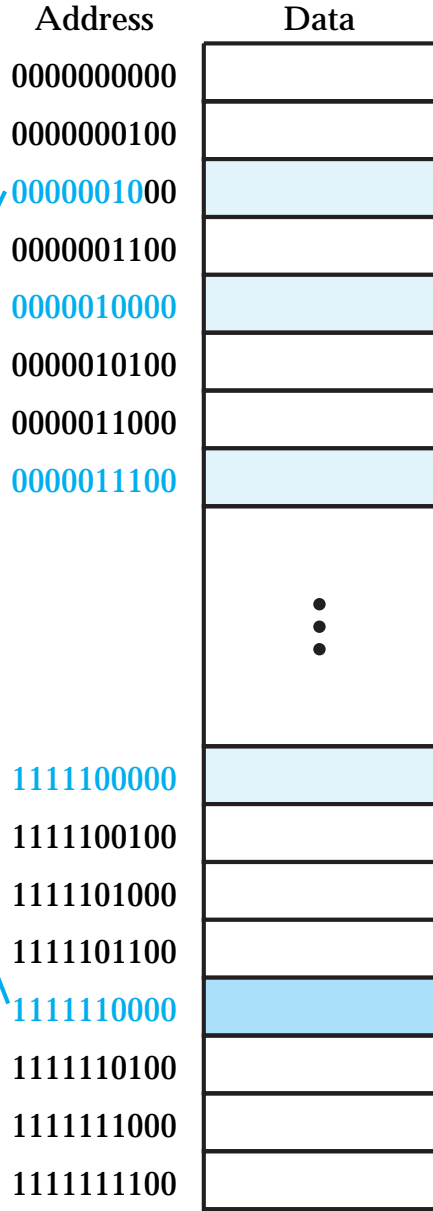
(b) Cache mapping



(a) Memory address

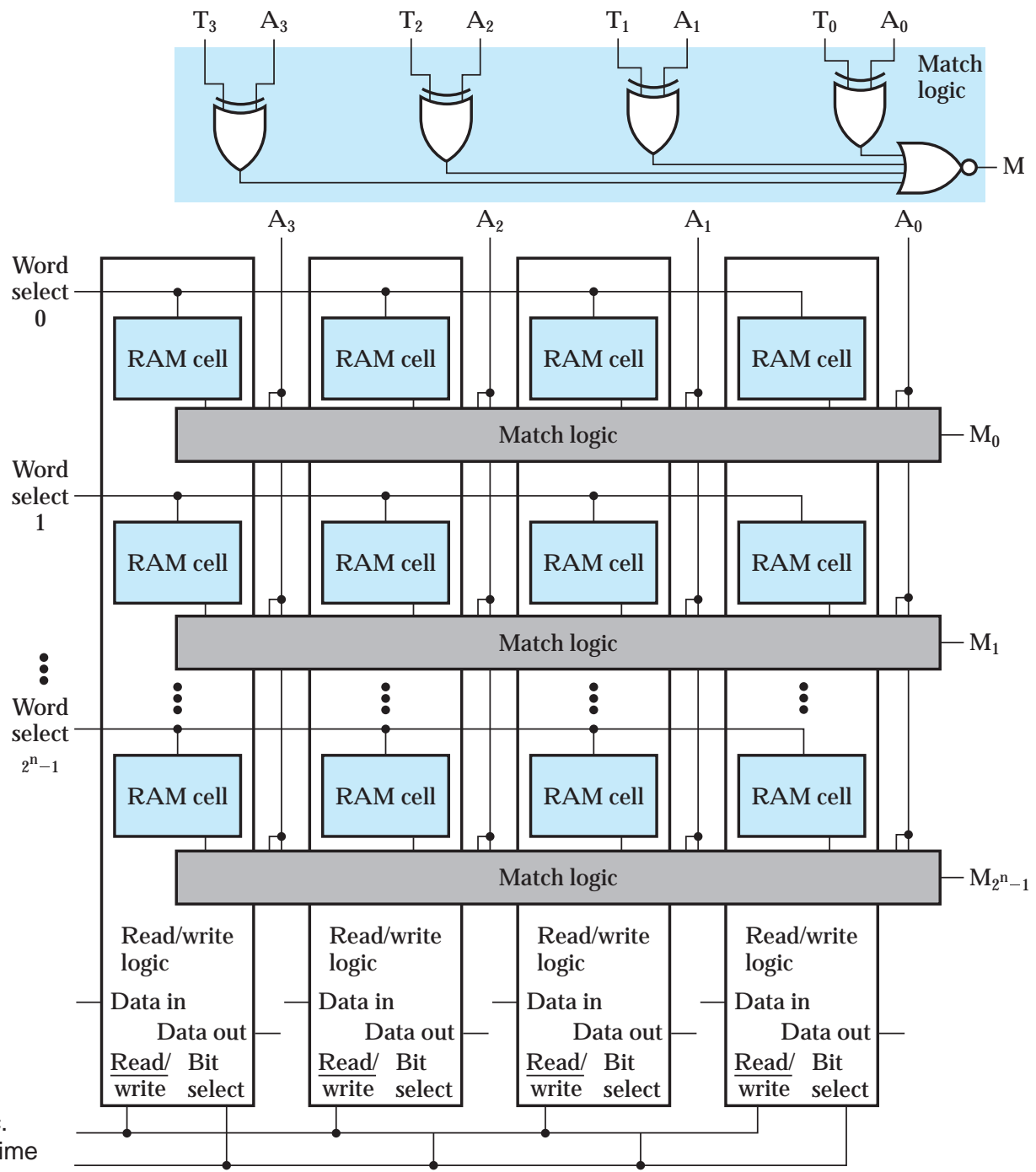


Cache

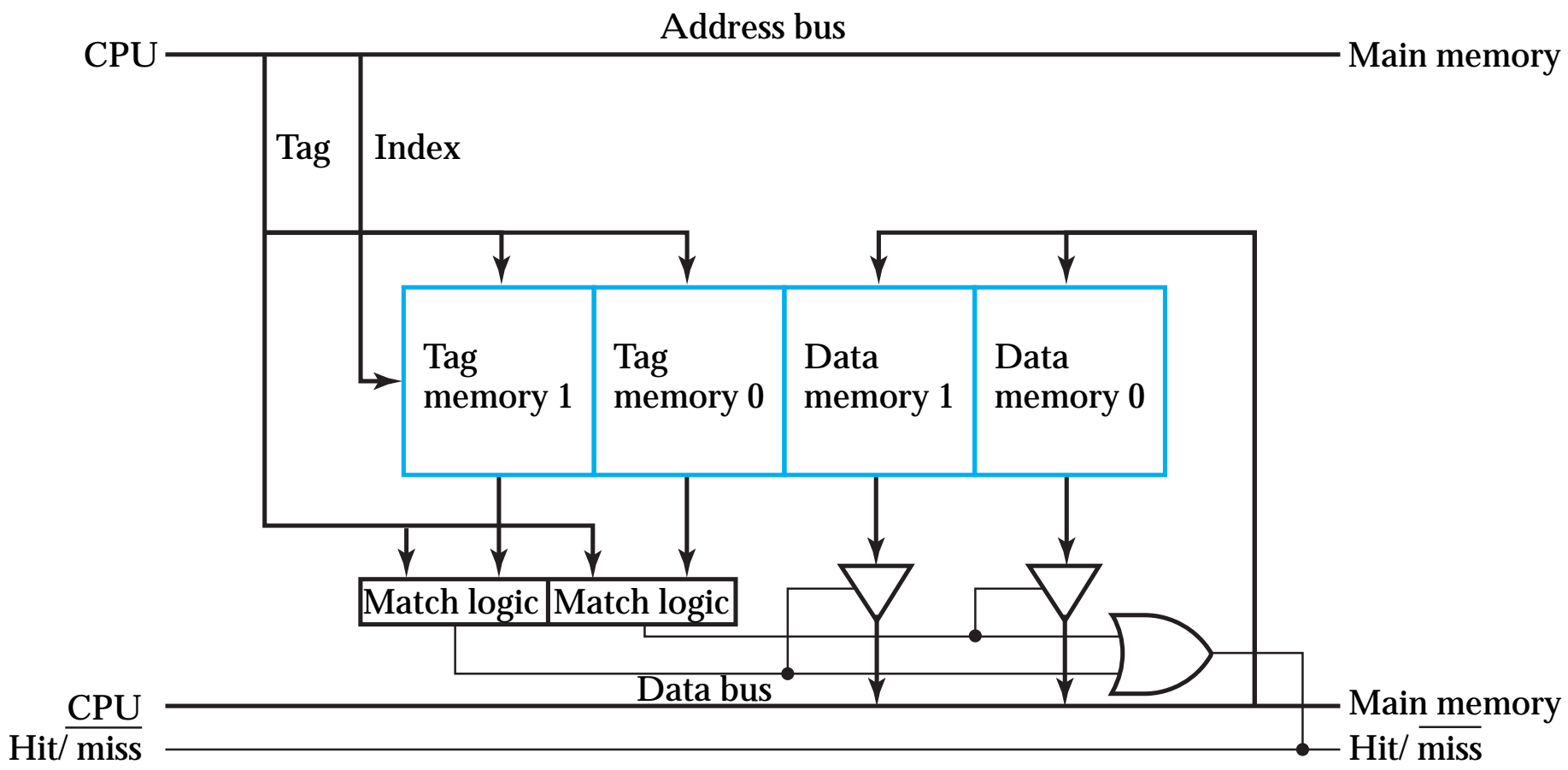


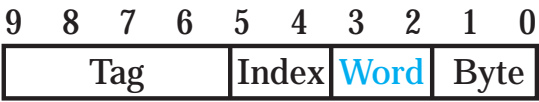
Main memory

(b) Cache mapping

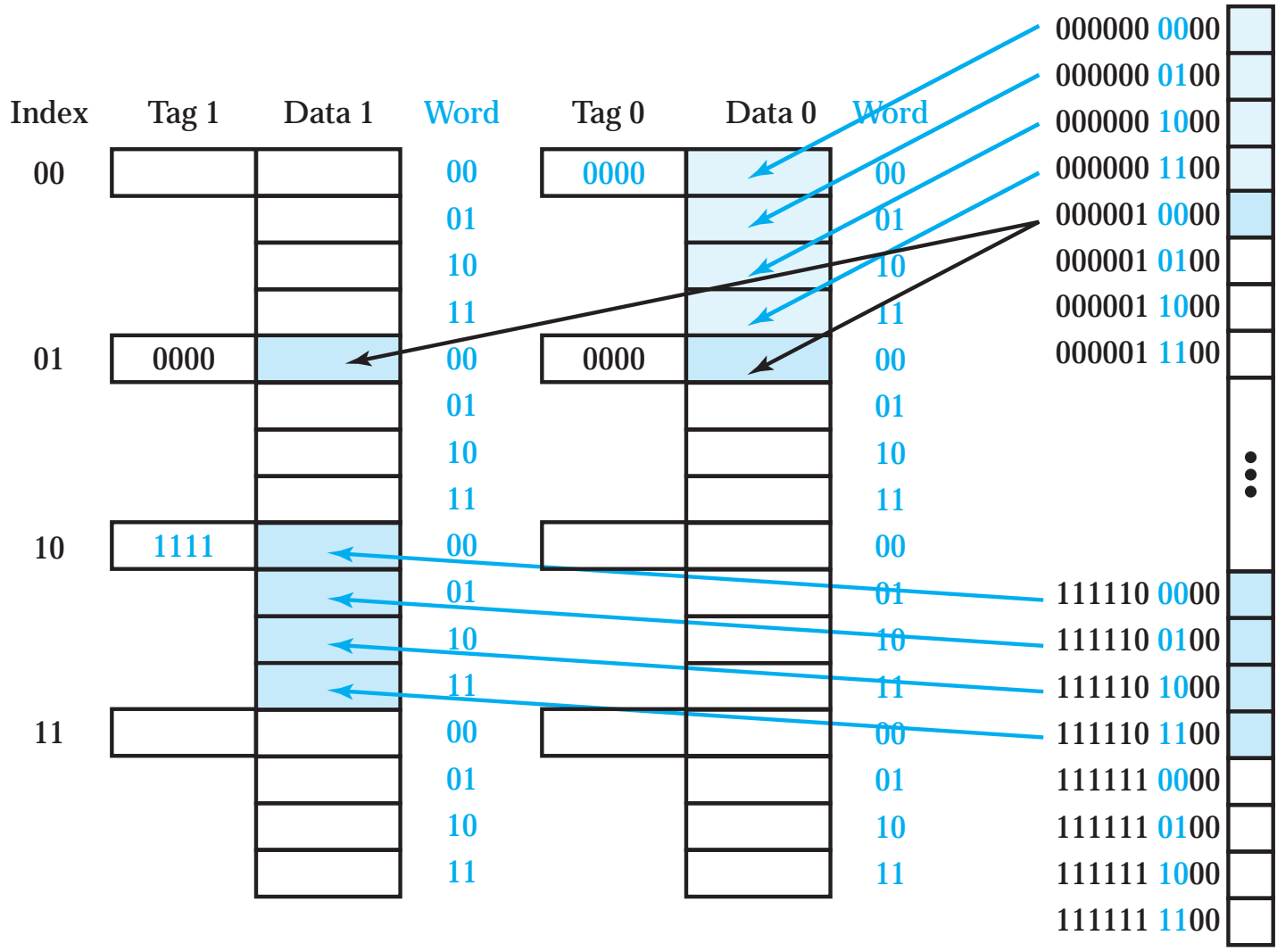








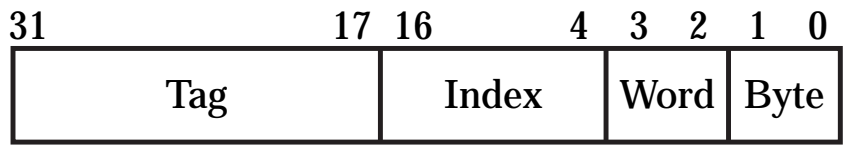
(a) Memory address



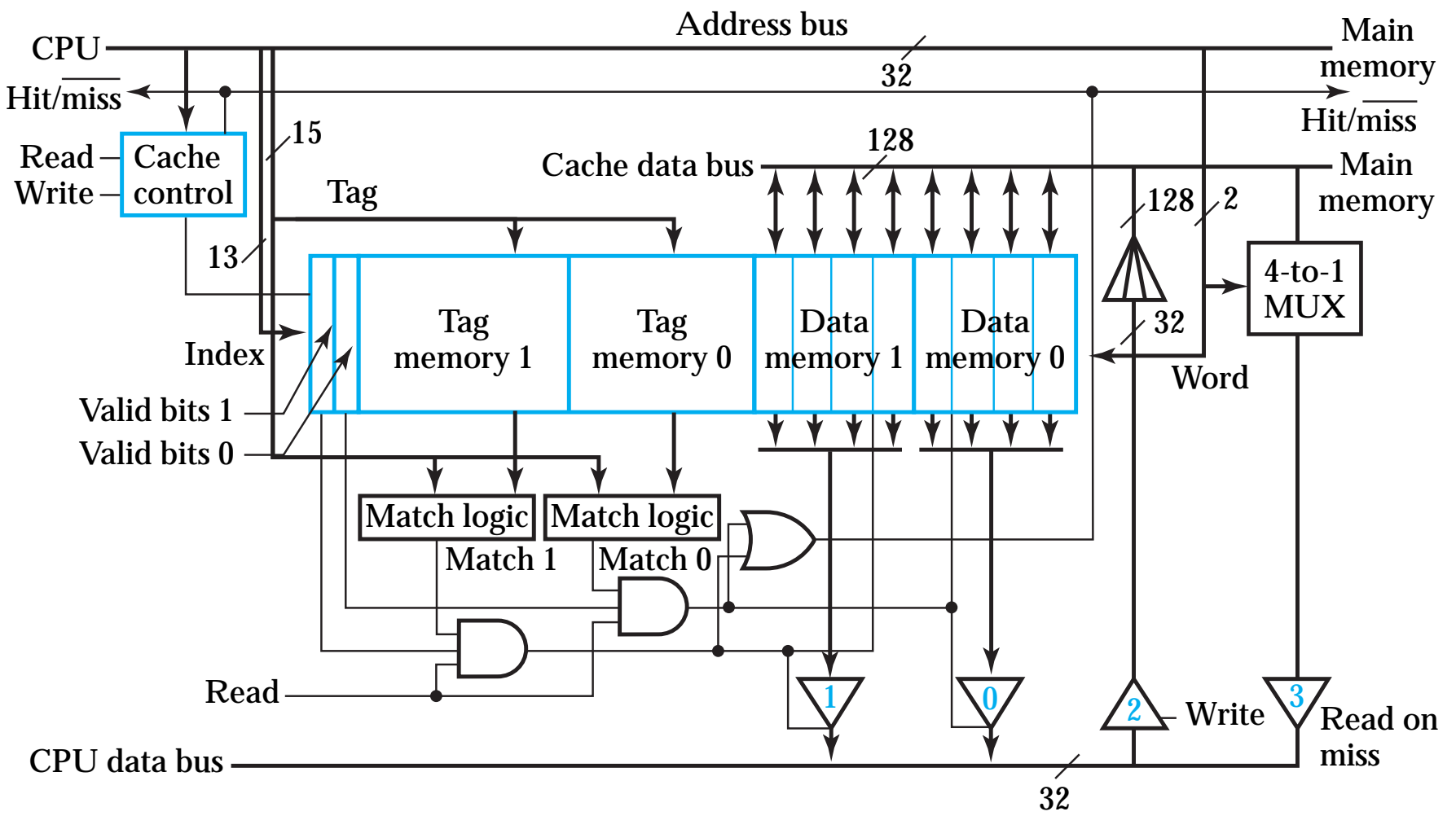
Cache

Main memory

(b) Cache mapping



(a) Memory address



(b) Cache diagram

